



Jawaharlal Nehru Technological University, Anantapur
STATEMENT OF MARKS / GRADE
MASTER OF TECHNOLOGY (VLSI / VLSI DESIGN / VLSI SYSTEM DESIGN)
(II Semester) EXAM SEPTEMBER - 2023



Roll No. 22BC1D5703 **Enrollment/Reg No.** 22BC1D5703 **Category** REGULAR

Name SOUJANYA BOJJIREDDY

Father's Name BOJJIREDDY RAJASEKARA REDDY **Mother's Name** BOJJIREDDY KRISHNAVENI

College/Deptt KORMCE-THADIGOTLA-KADAPA

Sub Code	Subject/Papers	Max Marks			Marks Obtained			Grade	Credit
		Th	In	Pr	Th	In	Pr		
21D57203C	MEMS SYSTEM DESIGN	60	40		26	37		C	3
21D57207	TECHNICAL SEMINAR		100			97	0	S	2
21D57201	CMOS MIXED SIGNAL IC DESIGN	60	40		30	38		C	3
21D57206	PHYSICAL DESIGN AUTOMATION LAB		40	60		38	58	S	2
21DAC201B	STRESS MANAGEMENT FOR YOGA AUDIT COURSE - II		40			38	0	Y	0
21D57205	CMOS MIXED SIGNAL IC DESIGN LAB		40	60		39	59	S	2
21D57204B	IOT AND ITS APPLICATIONS	60	40		37	38		B	3
21D57202	PHYSICAL DESIGN AUTOMATION	60	40		25	39		C	3
TOTAL									

SGPA : 8.17

RESULT : PASS



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