



Jawaharlal Nehru Technological University, Anantapur
STATEMENT OF MARKS / GRADE
MASTER OF TECHNOLOGY (VLSI / VLSI DESIGN / VLSI SYSTEM DESIGN)
(I Semester) EXAM SEPTEMBER - 2023



Roll No. 22BC1D5703 **Enrollment/Reg No.** 22BC1D5703 **Category** SUPPLEMENTARY

Name SOUJANYA BOJJIREDDY

Father's Name BOJJIREDDY RAJASEKARA REDDY **Mother's Name** BOJJIREDDY KRISHNAVENI

College/Deptt KORMCE-THADIGOTLA-KADAPA

Sub Code	Subject/Papers	Max Marks			Marks Obtained			Grade	Credit
		Th	In	Pr	Th	In	Pr		
21D57101	CMOS ANALOG IC DESIGN	60	40		24	38		C	3
21D57103C	CAD FOR VLSI	60	40		10	37		F	0
21D57102	CMOS DIGITAL IC DESIGN	60	40		35	38		B	3
TOTAL									

RESULT : **FAIL**



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