

## CLARIION FOUNDATIONS ASSESSMENT

Instructions: Select a single answer from all questions where the answers are preceded with parenthesis "( )". Select one or more answers from all questions where the answers are preceded with brackets "[ ]". For any matching questions, enter the letter of the corresponding matching item inside of the brackets provided.

| #                 | Question                                                                                                                                                                                                                                                                         |
|-------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| MAIN QUESTION SET |                                                                                                                                                                                                                                                                                  |
| 1.                | What is the maximum amount of cache memory per storage processor in the CX600?<br><br><input type="radio"/> 1GB<br><input type="radio"/> 2GB<br><input type="radio"/> 4GB<br><input type="radio"/> 8GB                                                                           |
| 2.                | What is the maximum number of host ports per storage processor in the CX600?<br><br><input type="radio"/> 1<br><input type="radio"/> 2<br><input type="radio"/> 4<br><input type="radio"/> 8                                                                                     |
| 3.                | Which software in the CLARiiON performs I/O handling, RAID algorithms, and cache management?<br><br><input type="radio"/> Base Software (FLARE)<br><input type="radio"/> Navisphere Analyzer<br><input type="radio"/> Navisphere CLI<br><input type="radio"/> Navisphere Manager |
| 4.                | Which component in the CLARiiON architecture physically connects the shelves of disks?<br><br><input type="radio"/> DPE2<br><input type="radio"/> LCC<br><input type="radio"/> SP<br><input type="radio"/> SPS                                                                   |
| 5.                | What is the maximum number of drives in a DAE2?<br><br><input type="radio"/> 5<br><input type="radio"/> 9<br><input type="radio"/> 10<br><input type="radio"/> 15                                                                                                                |
| 6.                | If a customer storage solution is driven by cost and moderate performance, which drive type would you recommend?<br><br><input type="radio"/> ATA<br><input type="radio"/> Fibre Channel<br><input type="radio"/> IDE<br><input type="radio"/> SCSI                              |
| 7.                | Which disks contain the vault area on a CX series CLARiiON array?<br><br><input type="radio"/> 0_0, 0_1, 0_2, 0_3, 0_4<br><input type="radio"/> 0_4, 0_5, 0_6, 0_7, 0_8<br><input type="radio"/> 0_a, 0_b, 0_c, 0_d, 0_e<br><input type="radio"/> 1_0, 1_1, 1_2, 1_3, 1_4        |

### CLARIION FOUNDATIONS ASSESSMENT (Continued)

| #                             | Question                                                                                                                                                                                                                                                                                   |
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| MAIN QUESTION SET (Continued) |                                                                                                                                                                                                                                                                                            |
| 8.                            | How is Cache memory on the Storage Processor allocated?<br><br><input type="radio"/> Page<br><input type="radio"/> Partition<br><input type="radio"/> Segment<br><input type="radio"/> Track                                                                                               |
| 9.                            | How is the CLARiiON write cache protected?<br><br><input type="radio"/> Dual Loops<br><input type="radio"/> Mirroring<br><input type="radio"/> NVRAM<br><input type="radio"/> Partitioning                                                                                                 |
| 10.                           | What CLARiiON process improves sequential read performance?<br><br><input type="radio"/> Formatting<br><input type="radio"/> Lazy read<br><input type="radio"/> Masking<br><input type="radio"/> Prefetching                                                                               |
| 11.                           | What condition forces the Storage Processor to <b>NOT</b> acknowledge write requests until memory has been released?<br><br><input type="radio"/> Forced Flushing<br><input type="radio"/> Idle destage<br><input type="radio"/> Idle Flushing<br><input type="radio"/> Watermark Flushing |
| 12.                           | When an event could compromise the integrity of data in the write cache, where does the Storage Processor dump the cache data?<br><br><input type="radio"/> Any Disk<br><input type="radio"/> PSM<br><input type="radio"/> Read cache<br><input type="radio"/> The Vault                   |
| 13.                           | What is the number of CPUs on each Storage Processor in the CX600?<br><br><input type="radio"/> 1<br><input type="radio"/> 2<br><input type="radio"/> 4<br><input type="radio"/> 8                                                                                                         |
| 14.                           | What is the maximum number of drives supported by the CX400?<br><br><input type="radio"/> 30<br><input type="radio"/> 60<br><input type="radio"/> 120<br><input type="radio"/> 240                                                                                                         |

### CLARIION FOUNDATIONS ASSESSMENT (Continued)

| #                             | Question                                                                                                                                                                                                                                                                                                                  |
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| MAIN QUESTION SET (Continued) |                                                                                                                                                                                                                                                                                                                           |
| 15.                           | <p>Which EMC Navisphere software provides a graphical interface to configure either a single or multiple CLARiiON arrays?</p> <p><input type="radio"/> Analyzer<br/><input type="radio"/> DART<br/><input type="radio"/> Manager<br/><input type="radio"/> Monitor</p>                                                    |
| 16.                           | <p>Which CLARiiON process builds LUNS onto RAID Groups?</p> <p><input type="radio"/> Auto Assignment<br/><input type="radio"/> Binding<br/><input type="radio"/> Masking<br/><input type="radio"/> Partitioning</p>                                                                                                       |
| 17.                           | <p>Your CLARiiON customer has described a storage solution that requires a synchronous copy of data on a second CLARiiON array. Which EMC product would you recommend?</p> <p><input type="radio"/> MirrorView<br/><input type="radio"/> SnapView<br/><input type="radio"/> SRDF<br/><input type="radio"/> TimeFinder</p> |

[Click here to view Answer Key](#)